

Figure 1. Prior Art

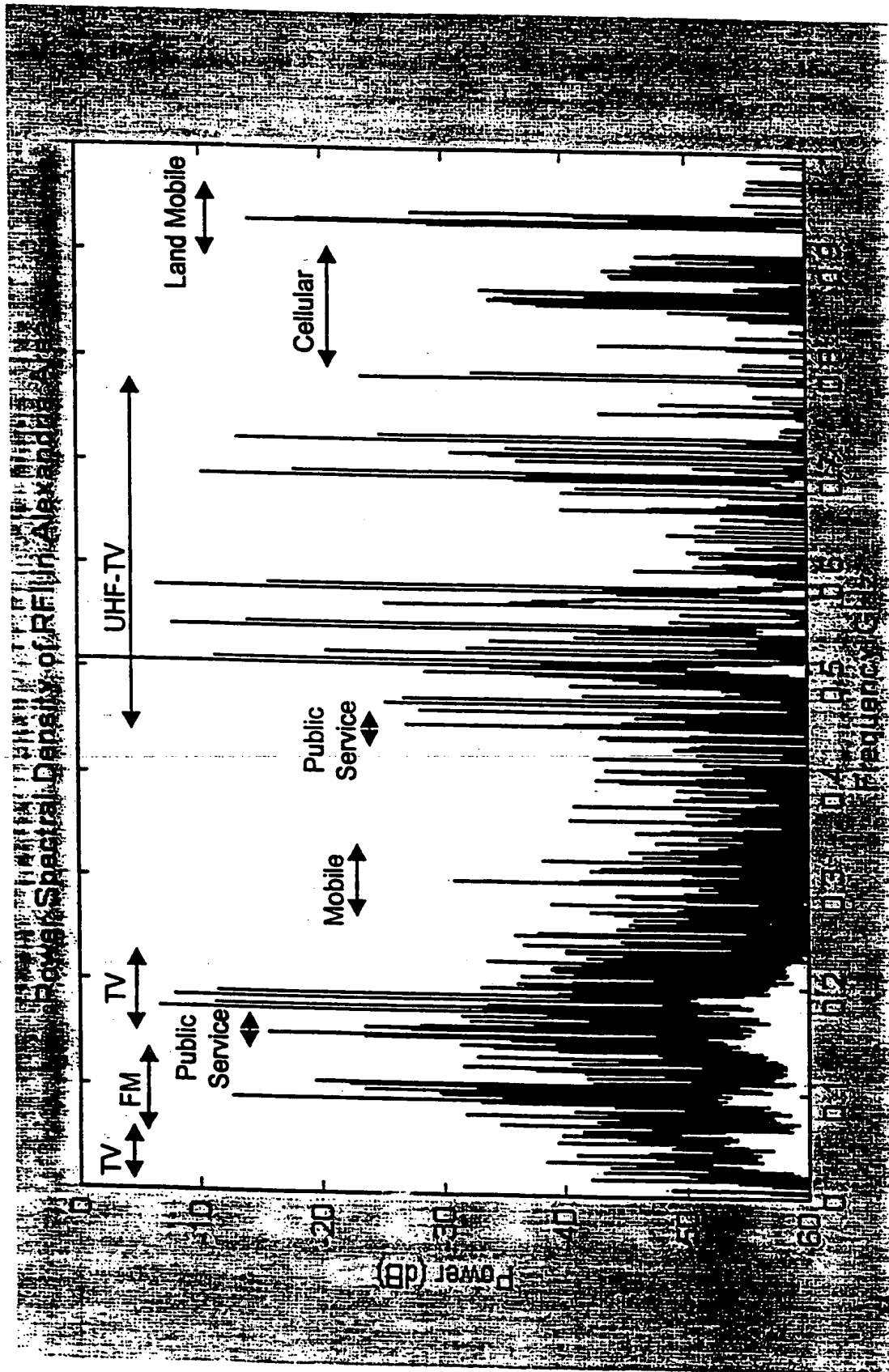


FIG. 2
PRIOR ART

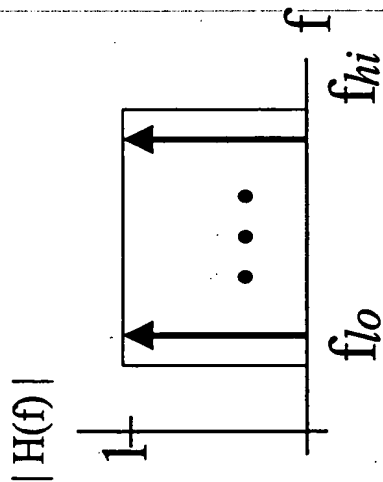


Figure 3(a)

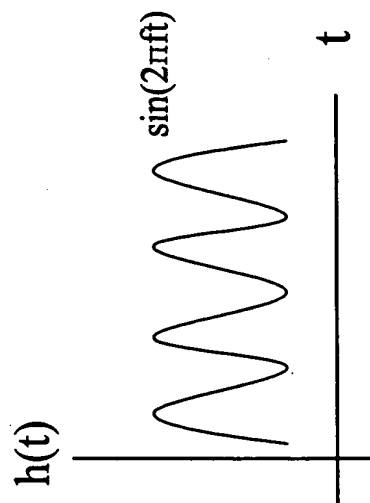


Figure 3(b)

Prior Art

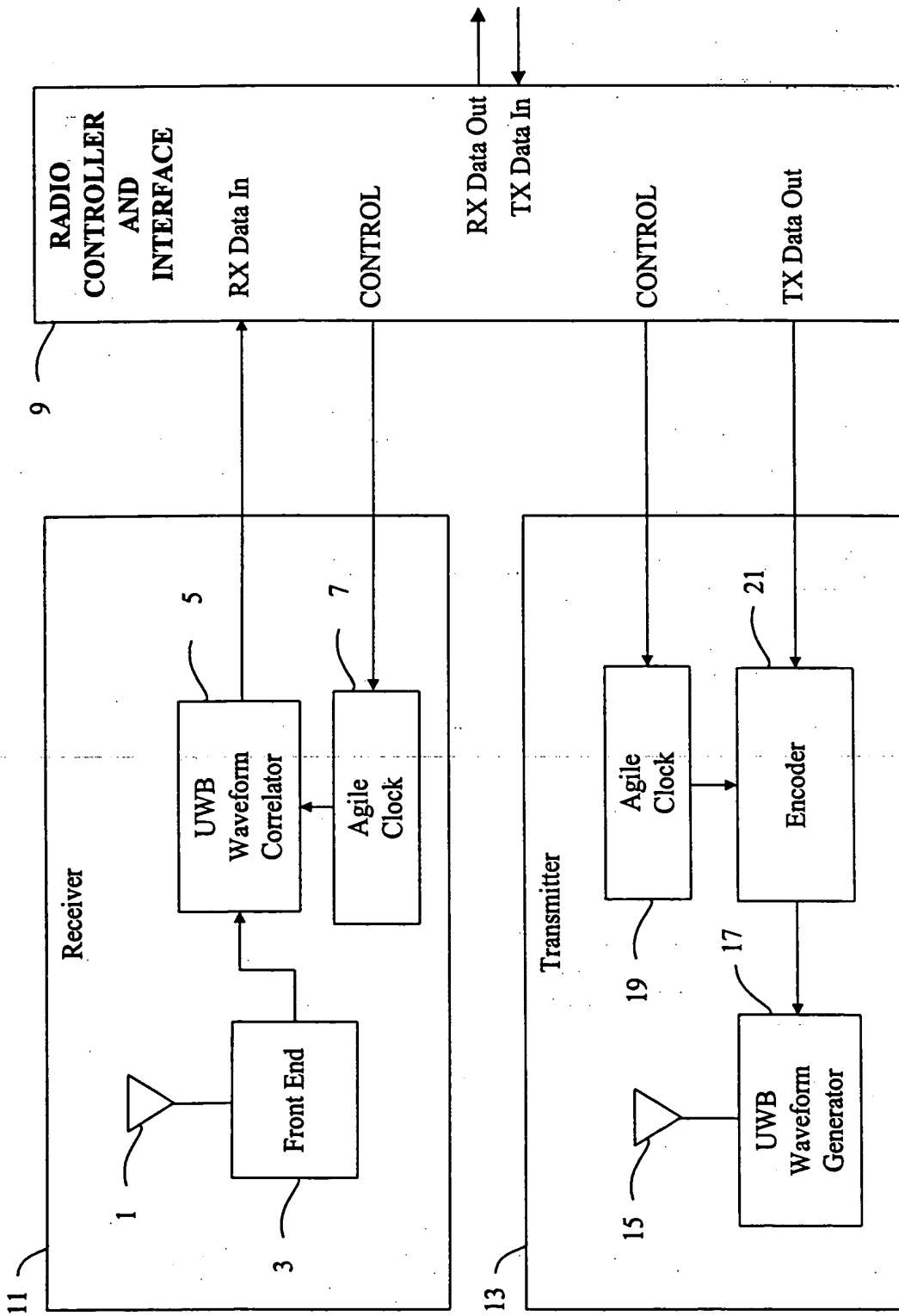


Figure 4(a)

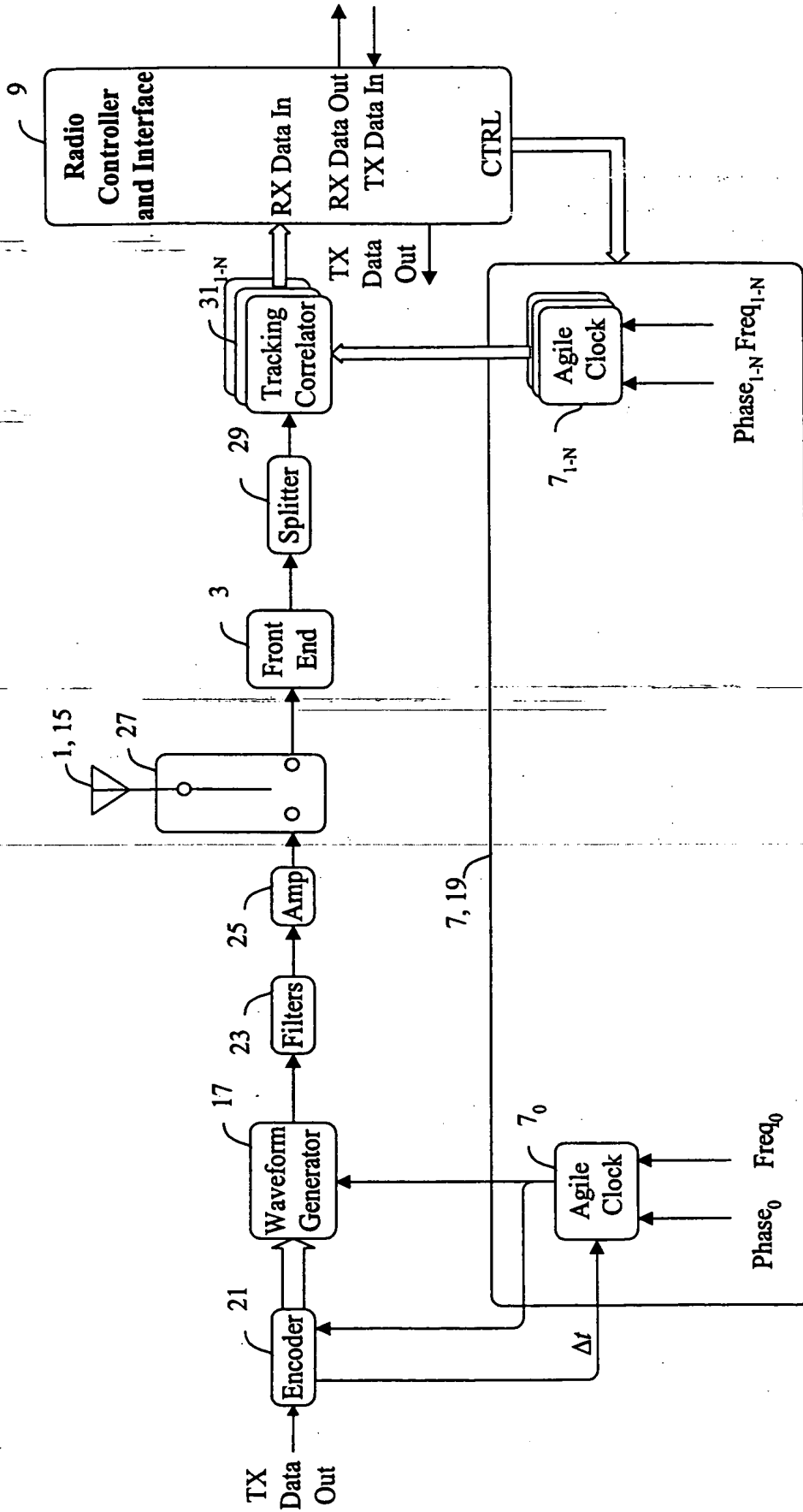


Figure 2 4(b)

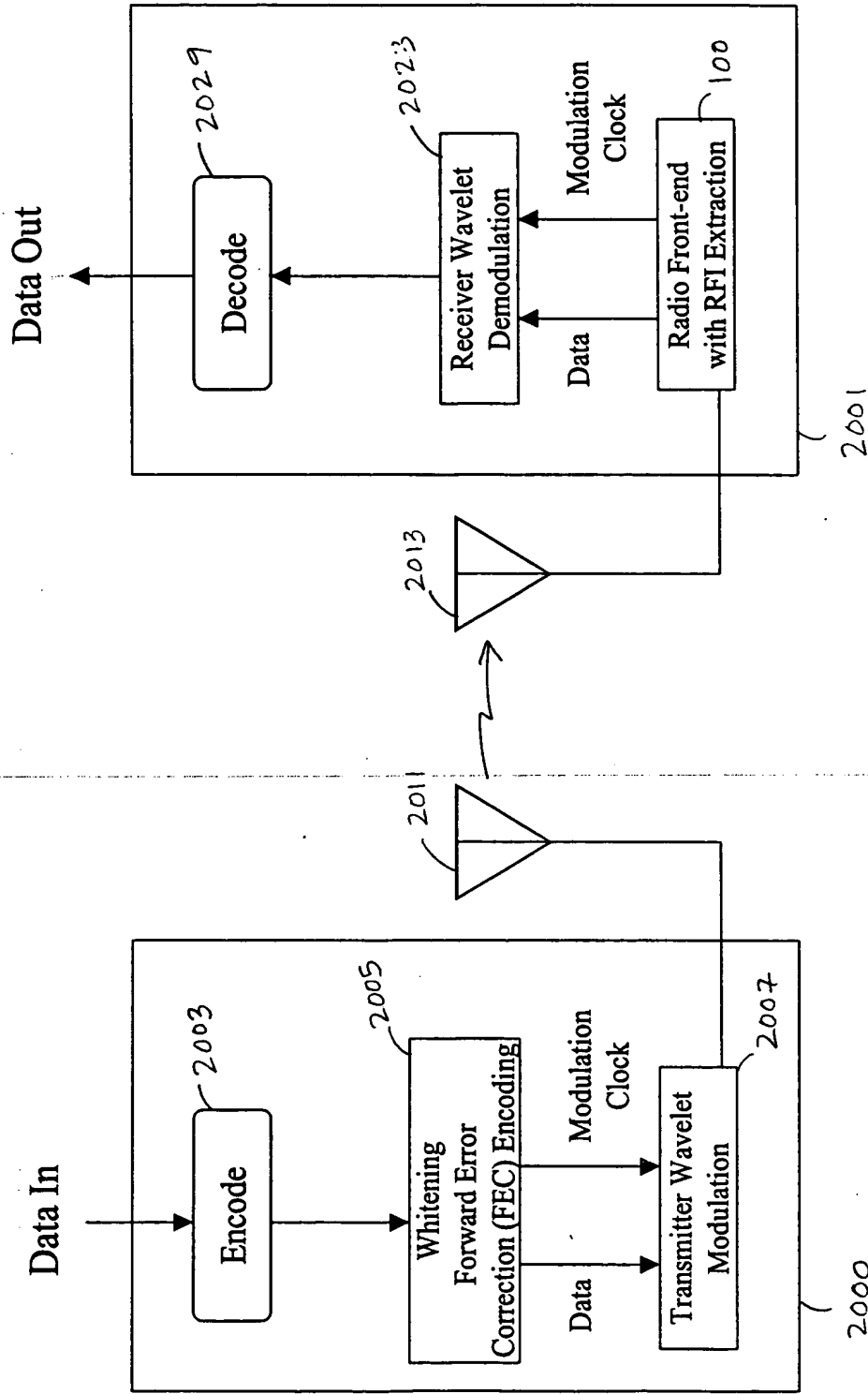


Figure 4A 4(c)

Network Characteristic



Figure 4B 4(d)

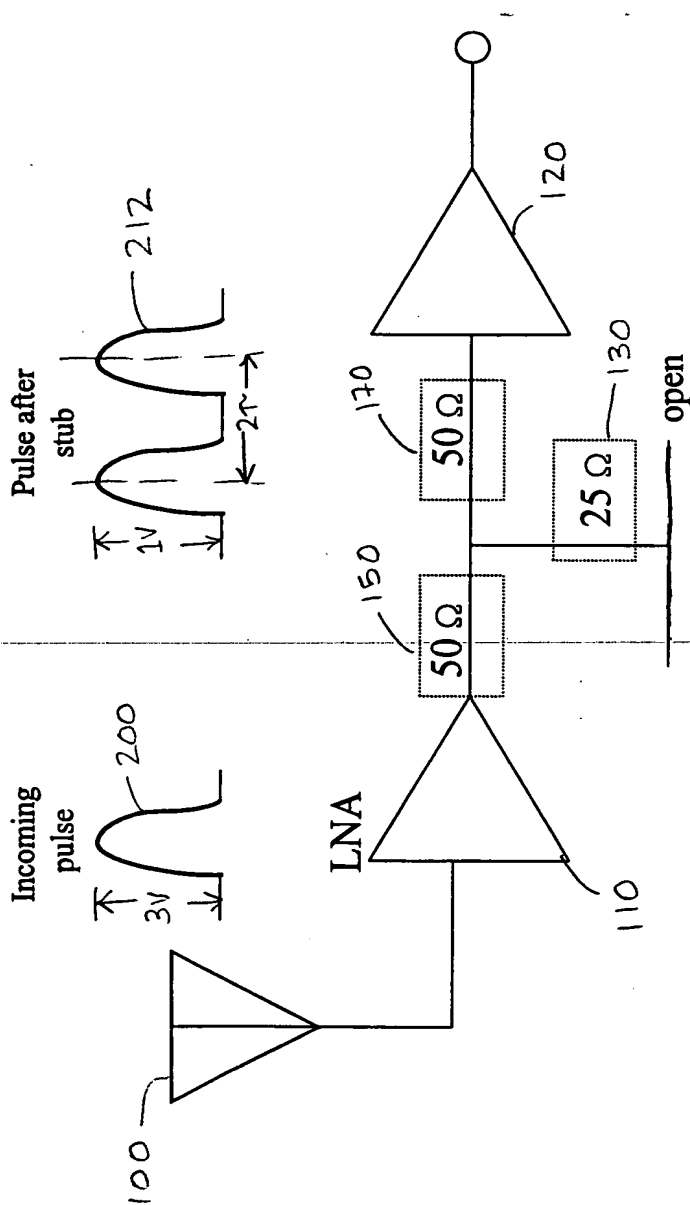


Figure 4C 4(e)

Figure 5(a)

Figure 5(b)

Figure 5(c)

Figure 5(d)

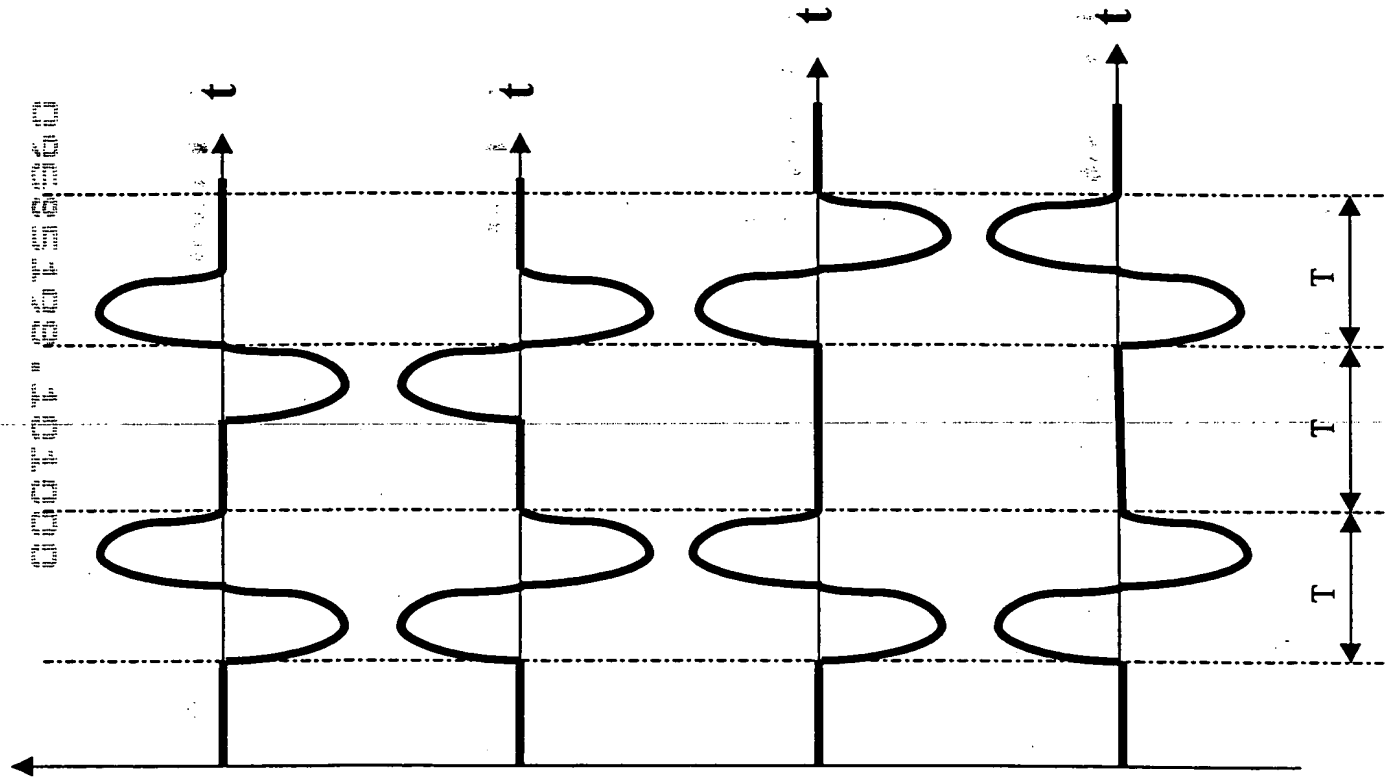


Figure 6(a)

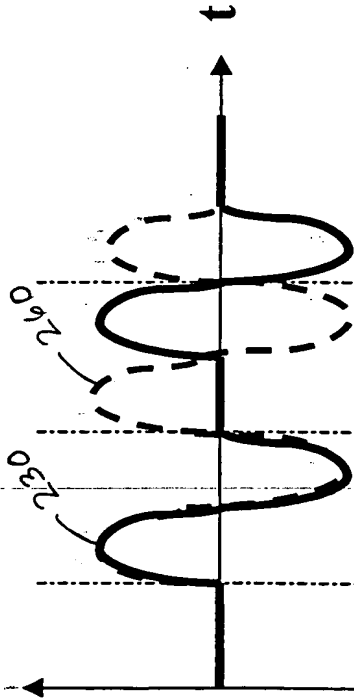
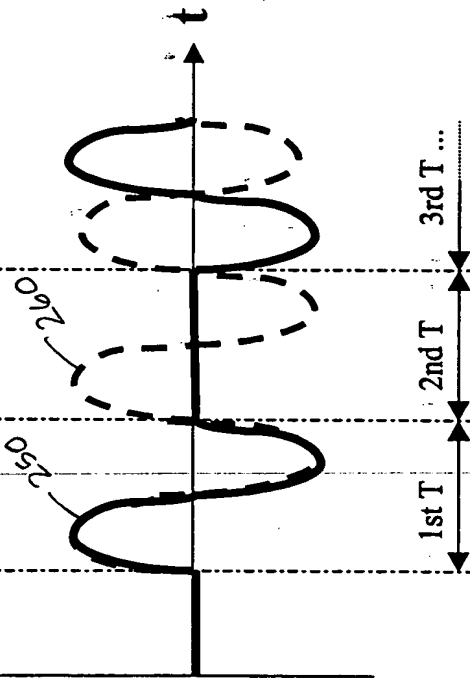


Figure 6(b)



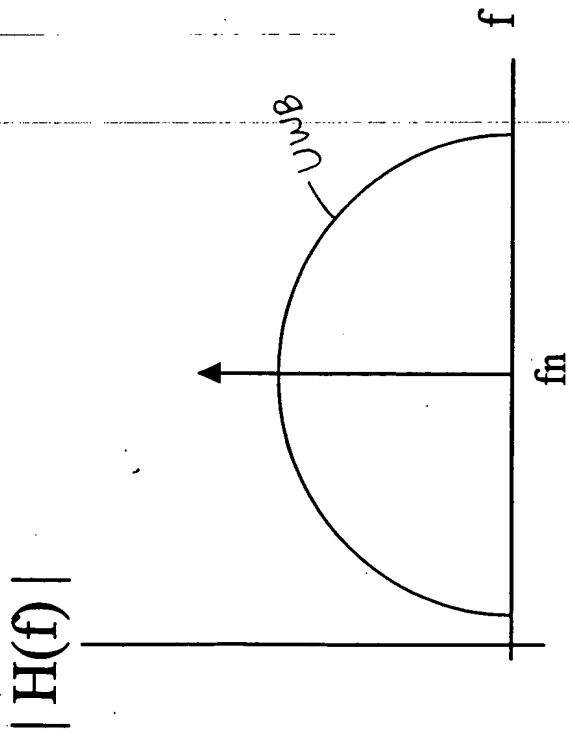


Figure 7(a)

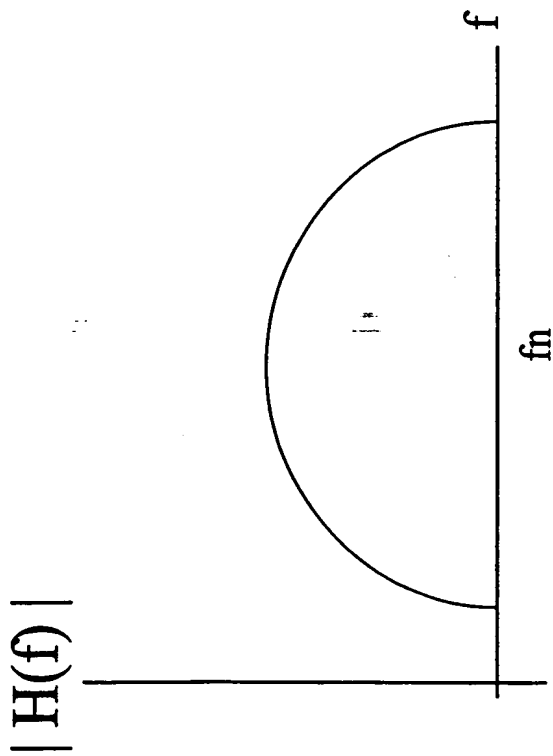


Figure 7(b)

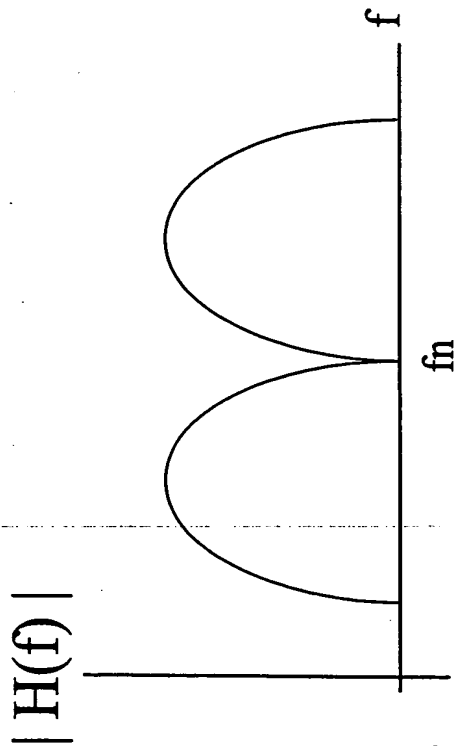


Figure 7(c)

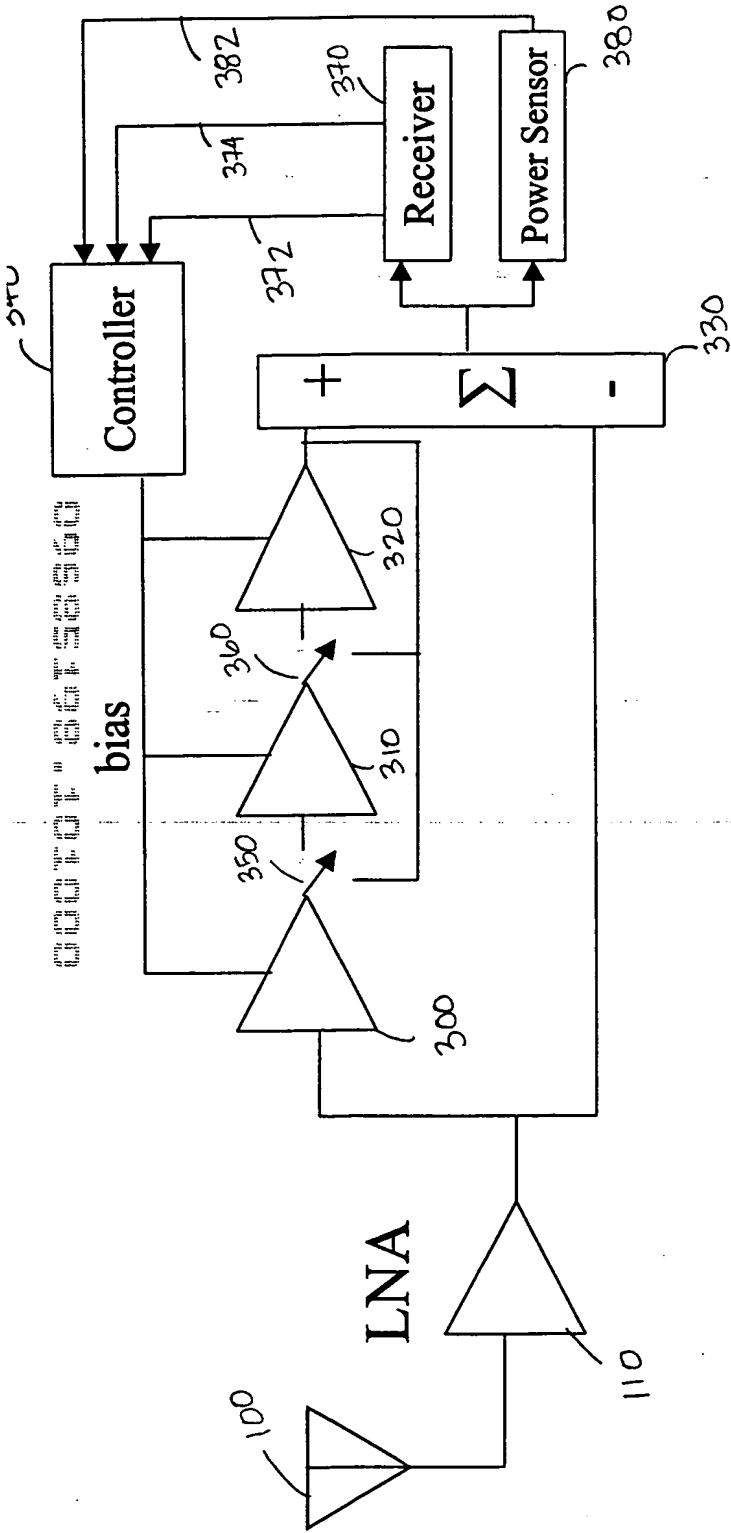


Figure 8A

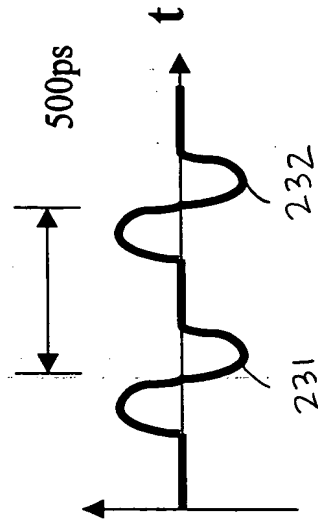


Figure 8B

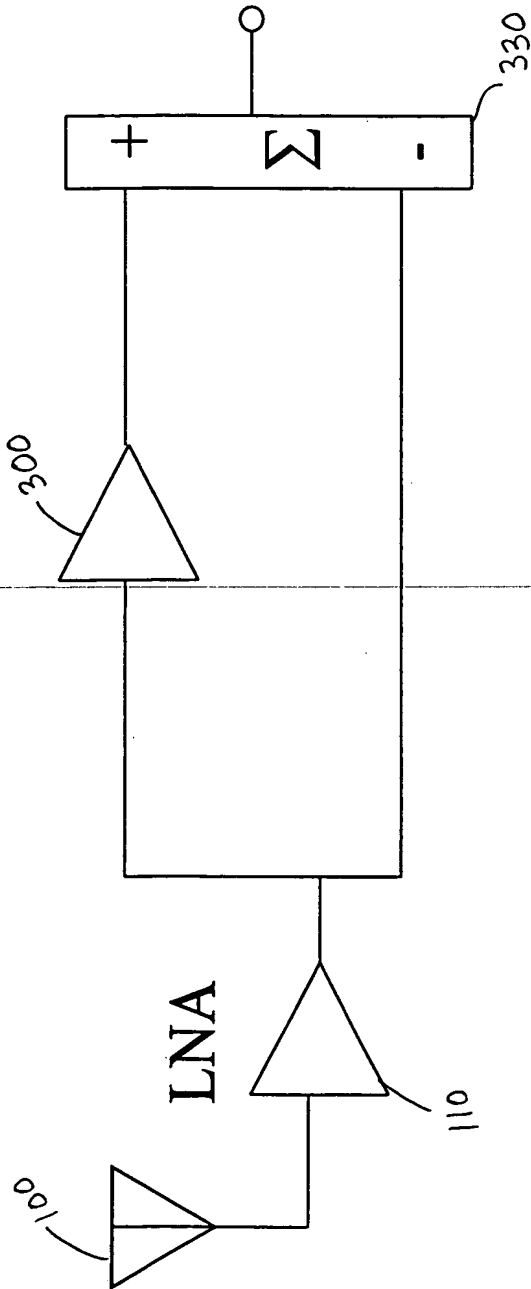


Figure 8C

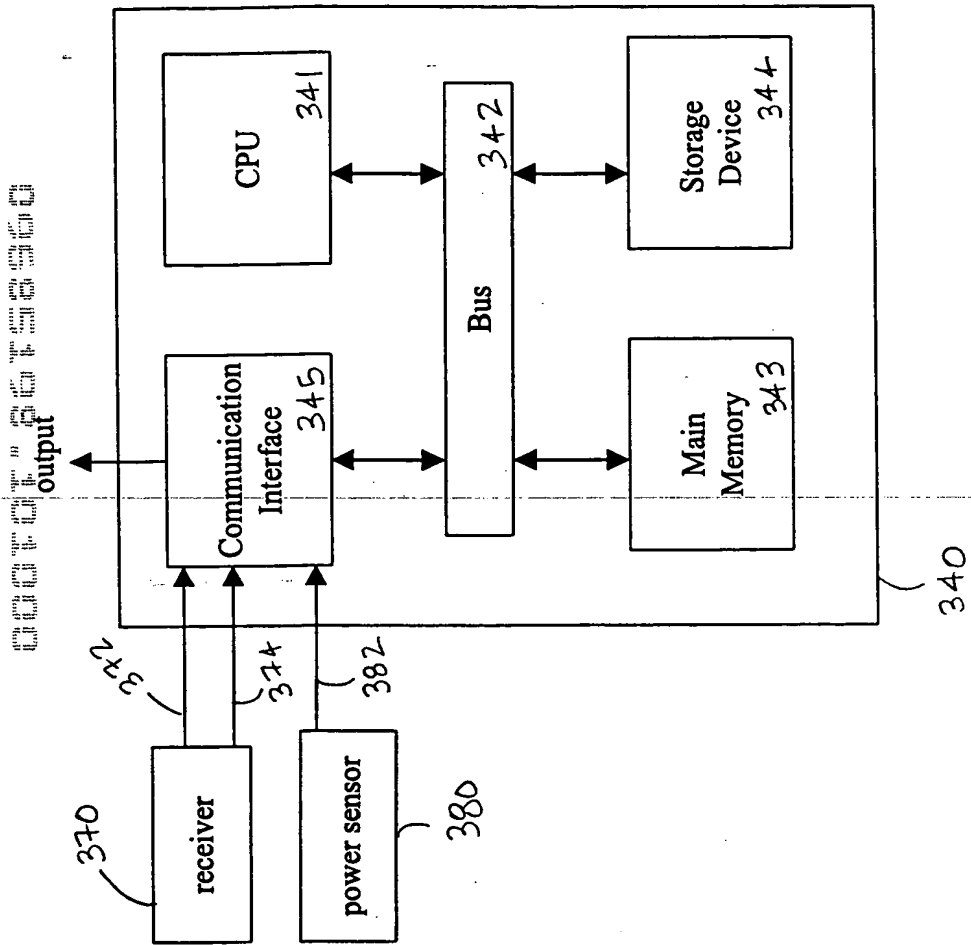


Figure 9

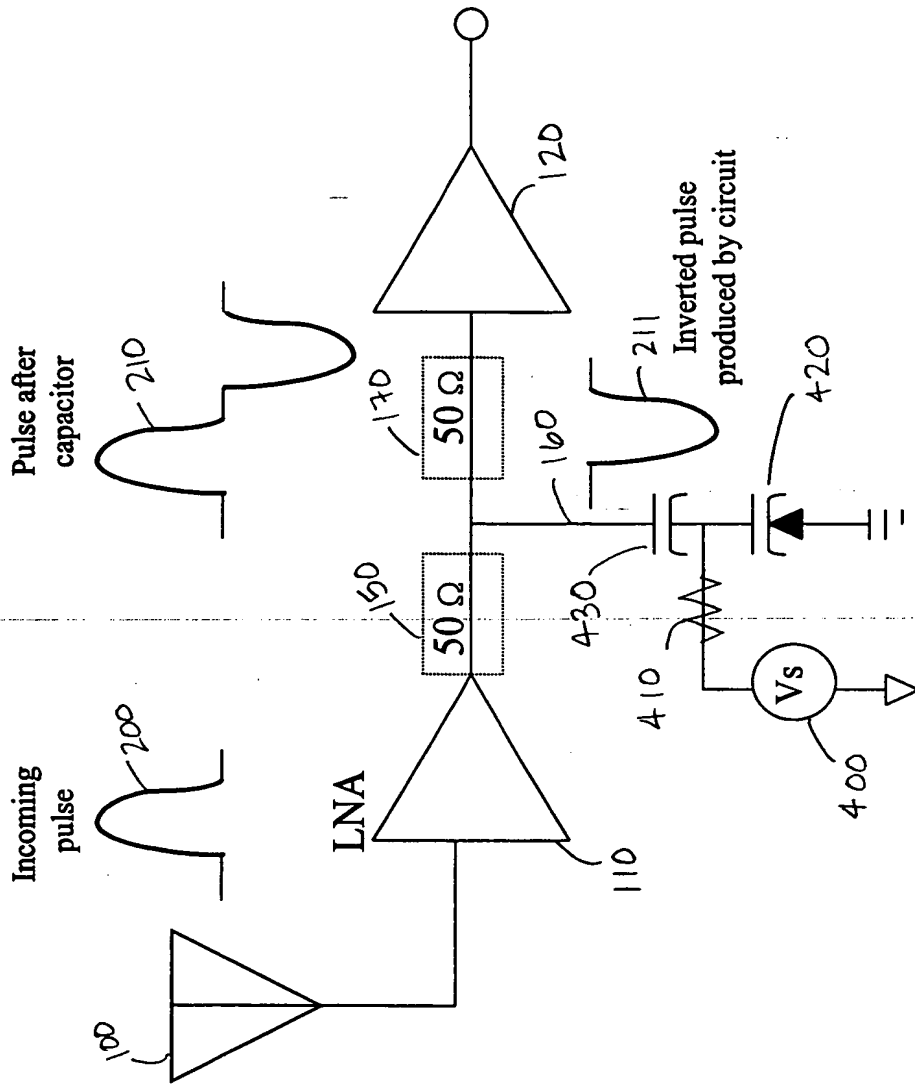
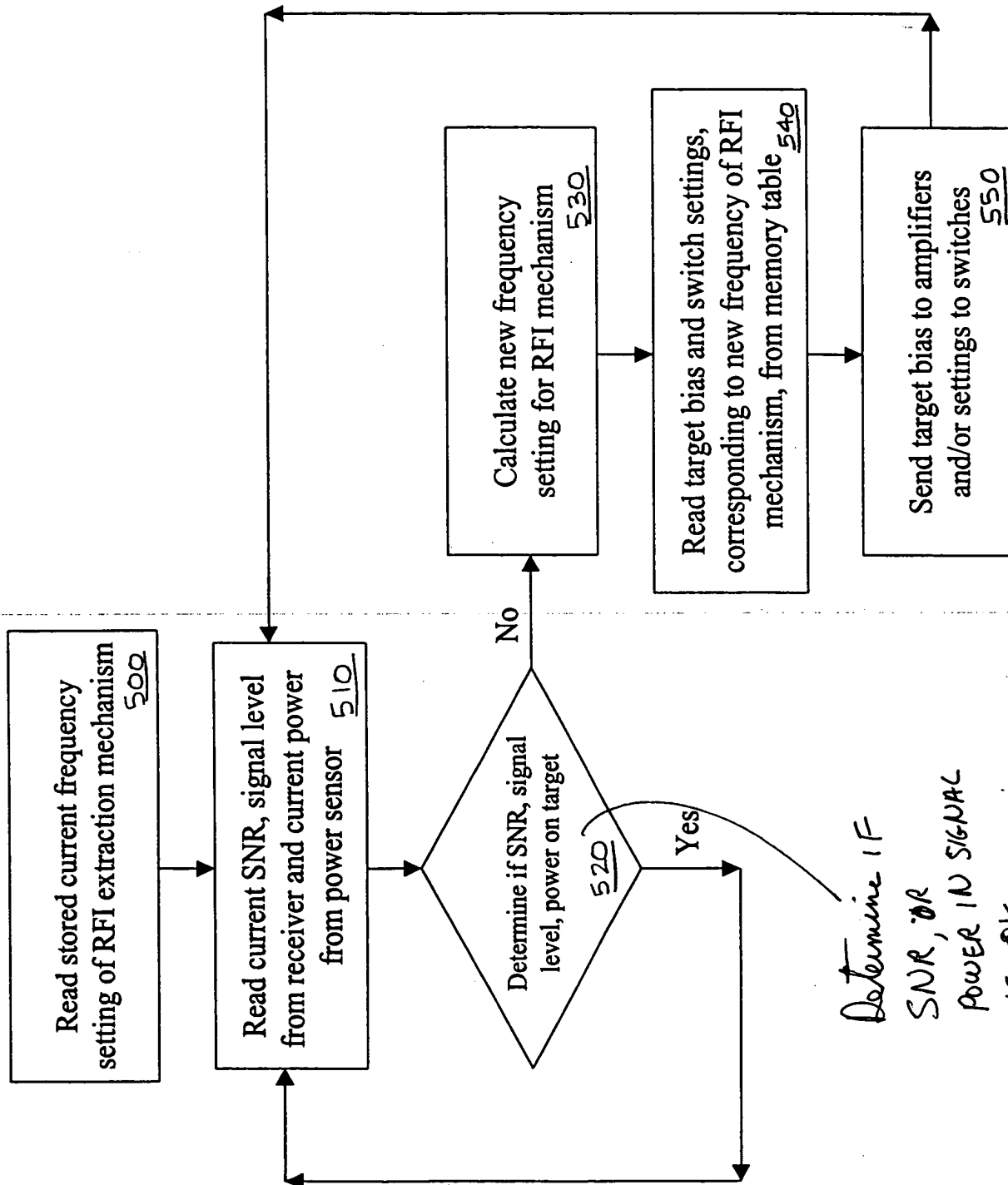


Figure 10



*Determine if
SNR, or
power in signal
is OK*

Figure 11

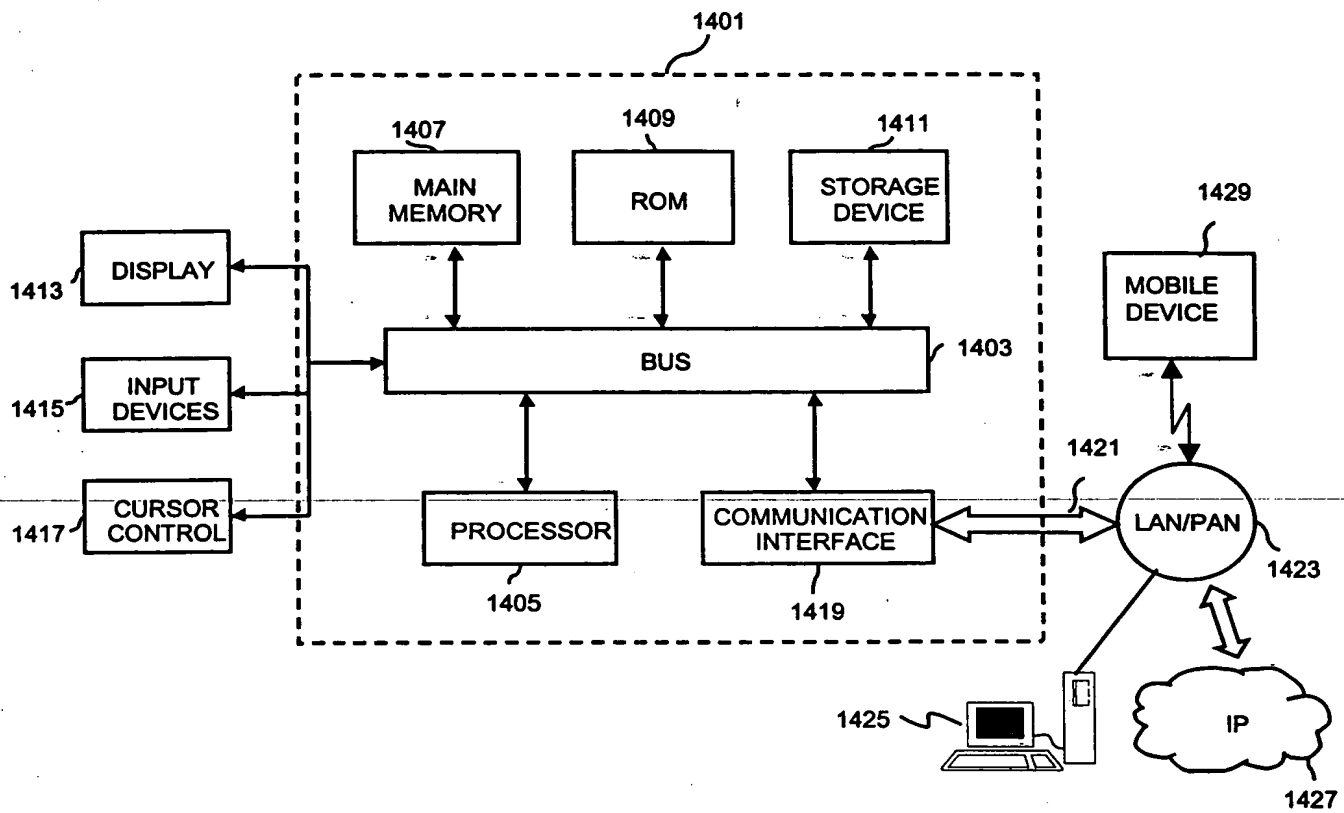


FIG. 12

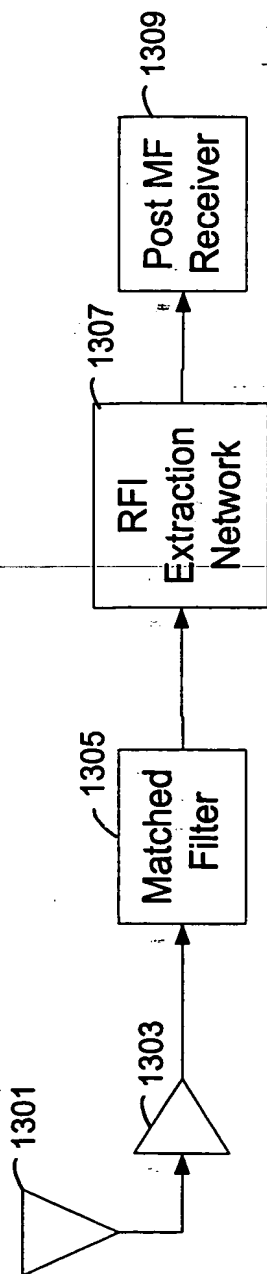


FIG. 13

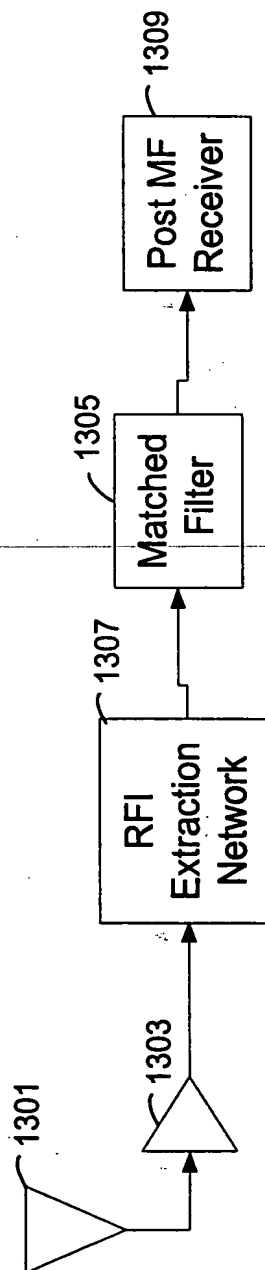


FIG. 14

